

0.8 A sensitive gate SCRs

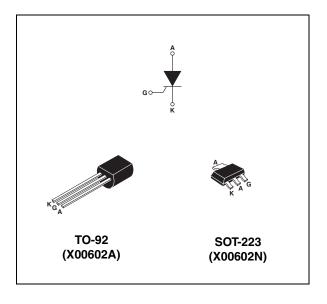
Features

- I_{T(RMS)} = 0.8 A
- $V_{DRM}/V_{RRM} = 600 \text{ V}$
- I_{GT} = 200 μA

Description

Thanks to highly sensitive triggering levels, the X006 SCR series is suitable for all applications where the available gate current is limited, such as ground fault circuit interrupters, overvoltage crowbar protection in low power supplies, capacitive ignition circuits, etc.

Available in though-hole or surface-mount packages, these devices are optimized in forward voltage drop and inrush current capabilities, for reduced power losses and high reliability in harsh environments.



Characteristics X006

1 Characteristics

Table 1. Absolute ratings (limiting values)

Symbol	Parameter			Value	Unit
I _{T(RMS)}	RMS on-state current (180 °Conduction angle)	TO-92	T _I = 85 °C	0.8	А
		SOT-223	T _{tab} = 100 °C		
IT.	Average on state current (190 °Conduction and la)	TO-92	T _I = 85 °C	0.5	А
IT _(AV)	Average on-state current (180 °Conduction angle)	SOT-223	T _{tab} = 100 °C		
I	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	T _j = 25 °C	10	Α
I _{TSM}	Non repetitive surge peak on state current	t _p = 10 ms		9	
l ² t	I ² t Value for fusing	t _p = 10 ms	T _j = 25 °C	0.4	A ² s
dI/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	F = 60 Hz	T _j = 125 °C	50	A/µs
I _{GM}	Peak gate current	t _p = 20 μs	T _j = 125 °C	1	Α
$P_{G(AV)}$	Average gate power dissipation $T_j = 125 ^{\circ}\text{C}$			0.1	W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C

Table 2. Electrical characteristics

Symbol	Test Conditions			Value	Unit
			MIN.	15	
I _{GT}	$V_D = 12 \text{ V}, R_L = 140 \Omega$		MAX.	200	μΑ
V _{GT}	V _{GT}		MAX.	0.8	V
V_{GD}	$V_D = V_{DRM,} \; R_L = 3.3 \; k\Omega$, $R_{GK} = 1 \; k\Omega$	T _j = 125 °C	MIN.	0.2	V
V _{RG}	I _{RG} = 10 μA		MIN.	5	V
I _H	I_T = 50 mA, R_{GK} = 1 k Ω		MAX.	5	mA
ΙL	$I_G = 1 \text{ mA}, R_{GK} = 1 \text{ k}\Omega$		MAX.	6	mA
dV/dt	$V_D = 67\% V_{DRM}, R_{GK} = 1 k\Omega$	T _j = 125 °C	MIN.	25	V/µs
V _{TM}	I _{TM} = 1 A, tp = 380 μs	T _j = 25 °C	MAX.	1.35	V
V _{t0}	Threshold voltage	T _j = 125 °C	MAX.	0.85	V
R _d	Dynamic resistance	T _j = 125 °C	MAX.	245	mΩ
I _{DRM}	$V_{DRM} = V_{RRM}$, $R_{GK} = 1 \text{ k}\Omega$	T _j = 25 °C	MAX.	1	
I _{RRM}		T _j = 125 °C	MAX.	100	μΑ

X006 Characteristics

Table 3. Thermal resistances

Symbol	Parameter			Value	Unit
R _{th(j-a)}	Junction to embient (DC)		TO-92	150	
	Junction to ambient (DC)	$S = 5 \text{ cm}^2$	SOT-223	60	00/14/
R _{th(j-l)}	Junction to lead (DC)		TO-92	70	°C/W
R _{th(j-t)}	Junction to tab (DC)		SOT-223	30	

Figure 1. Maximum average power dissipation versus average on-state current

Figure 2. Average and DC on-state current versus case temperature (TO-92)

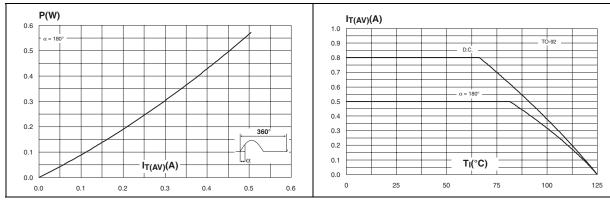
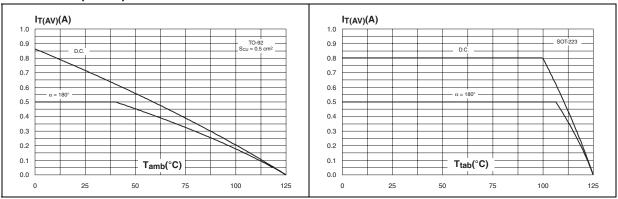


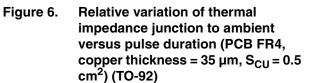
Figure 3. Average and D.C. on-state current versus ambient temperature (epoxy printed circuit board FR4, copper thickness = 35 μ m, S_{CU} = 0.5 cm²) (TO-92)

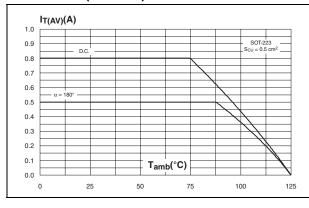
Figure 4. Average and DC on-state current versus case temperature (SOT-223)



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Figure 5. Average and DC on-state current versus ambient temperature (epoxy PCB FR4, copper thickness = 35 μ m, S_{CU} = 5 cm²) (SOT-223)





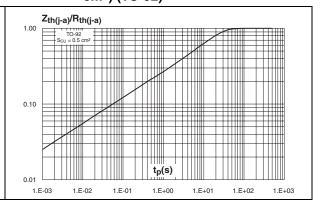
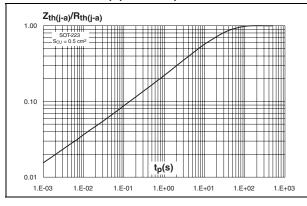


Figure 7. Relative variation of thermal impedance junction to ambient versus pulse duration (PCB FR4, copper thickness = 35 μ m, S_{CU} = 0.5 cm²) (SOT-223)

Figure 8. Thermal resistance junction to ambient versus copper surface under tab (PCB FR4, copper thickness = 35 µm) (SOT-223)



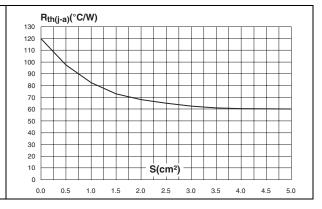
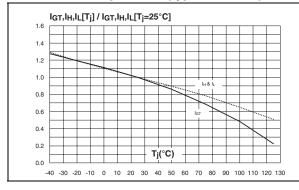
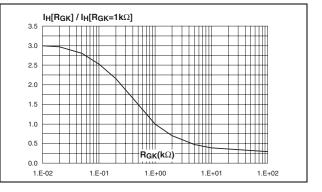


Figure 9. Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)

Figure 10. Relative variation of holding current versus gate-cathode resistance (typical values)





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Figure 11. Relative variation of dV/dt immunity Figure 12. versus gate-cathode resistance (typical values)

Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values)

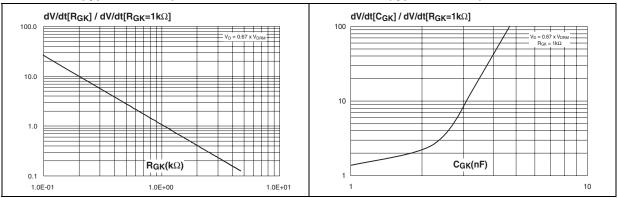


Figure 13. Surge peak on-state current versus Figure 14. number of cycles

Non repetitive surge peak on-state current for a sinusoidal pulse with width t_{p} < 10ms, and corresponding value of $l^{2}t$

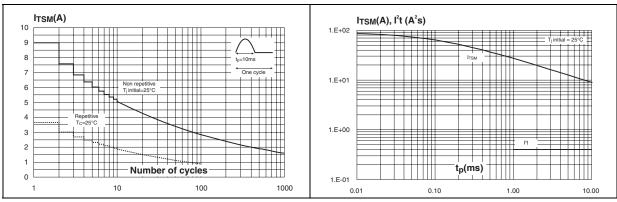
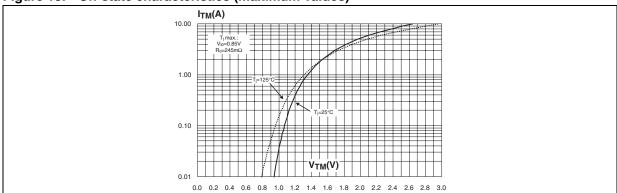
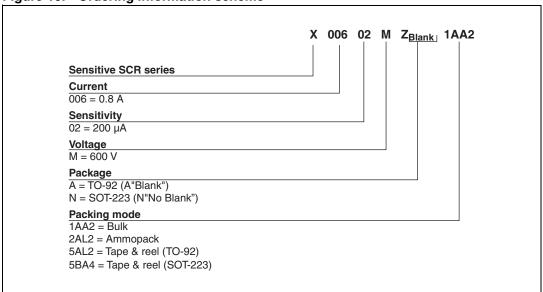


Figure 15. On-state characteristics (maximum values)



2 Ordering information scheme

Figure 16. Ordering information scheme



X006 Package information

3 Package information

Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Table 4. TO-92 (plastic) dimensions

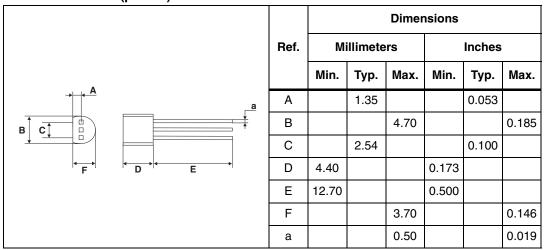
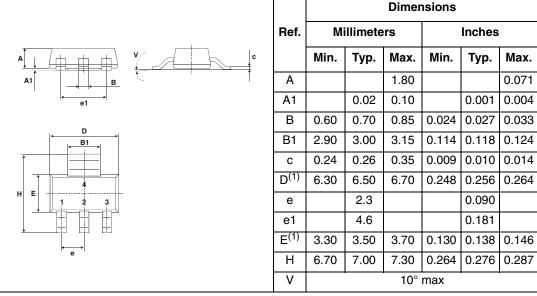


Table 5. SOT-223 dimensions



^{1.} Do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm (0.006inches)

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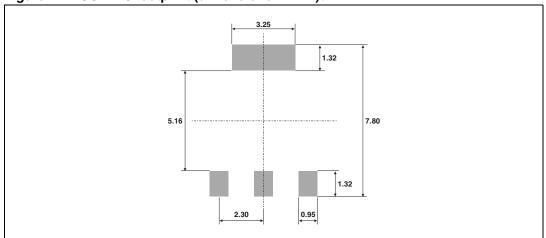


Figure 17. SOT-223 footprint (dimensions in mm)t

4 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode	
X00602MA 1AA2				2500	Bulk	
X00602MA 2AL2	X0602 MA	TO-92	0.2 g	2000	Ammopack	
X00602MA 5AL2				2000	Tape and reel	
X00602MN5BA4	X06 2M	SOT-223	0.12 g	1000	Tape and reel	

5 Revision history

Table 7. Document revision history

Date	Revision	Changes
Jan-2002	3	Last update.
08-Aug-2006	4	SOT-223 package added.
1-Apr-2008 5		Reformatted to current standards. Device X00605 removed. Updated dimensions in <i>Table 5</i> .

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